ANURAAG NANDAN V

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CAREER OBJECTIVE

M.Tech student specializing in VLSI Design and Embedded Systems with strong foundations in hardware design, digital systems, and Python-based data analysis. Seeking a Hardware Engineer Intern role to contribute to system validation, hardware testing, and data-driven engineering solutions, while applying knowledge of embedded systems and basic machine learning concepts.

EDUCATION

M.Tech – VLSI Design and Embedded Systems
Dayanand Sagar Institution, Bengaluru | 2025 – Present

B.E – Electronics and Communication Engineering
JSS Academy of Technical Education, Bengaluru | 2025

TECHNICAL SKILLS

Hardware & VLSI: Digital Design, RTL Concepts, CMOS Basics, Combinational & Sequential Circuits

HDL & FPGA Tools: Verilog, Basic VHDL, Xilinx Vivado

Platforms: ZedBoard (Zynq-7000 SoC)

Embedded Systems: Embedded Fundamentals, PLC Systems

Programming: Python, C

Concepts: Signal Processing, Data Analysis, Basic Machine Learning

INTERNSHIP EXPERIENCE

Intern – PLC Control Panel Design
S2D Process Solutions Pvt. Ltd., Bengaluru | Oct 2024 – Dec 2024
- Designed and wired PLC-based control panels
- Performed signal interfacing and validation
- Assisted in troubleshooting system-level issues
- Collaborated with engineering teams

Intern – Electronics Division
BHEL, Bengaluru | Sep 2024 – Oct 2024
• Studied traction electronics systems
• Observed PCB assembly and testing workflows
• Analyzed validation and quality processes
• Learned system-level integration

PROJECTS

Data-Driven Analysis of CMOS Gate Delay vs Transistor Sizing (W/L)
• Analyzed impact of W/L on CMOS delay
• Processed simulation data
• Used Python for analysis and visualization
• Applied regression for prediction
• Derived performance insights

FPGA-Based Priority Encoder Implementation (ZedBoard)
• Designed 4-bit priority encoder in Verilog
• Implemented using Vivado on ZedBoard
• Performed simulation and verification
• Validated outputs on hardware
• Debugged implementation issues

MSVD-Based Enhancement of Low-Quality CCTV Images
• Implemented MSVD using Python
• Improved image quality
• Applied signal processing techniques

STRENGTHS

Problem-solving, Quick learner, Analytical thinking, Team collaboration

LANGUAGES

English | Kannada | Hindi